

09/890226

Class	Subclass
ISSUE	

FILED UNDER 35 U.S.C. 371

PATENT NUMBER

## U.S. UTILITY Patent Application



O.I.P.E.

PATENT DATE

APPLICATION NO. 09/890226	CONT/PRIOR D F	CLASS 257	SUBCLASS	ART UNIT 2811	EXAMINER <i>Sebastian</i>
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APPLICANTS

Yves Reignoux  
Eric Daniel

TITLE

Integrated circuit device, electronic module for chip cards using said device and method for making same

PTO-2040  
12/99

## ISSUING CLASSIFICATION

ORIGINAL		CROSS REFERENCE(S)						
CLASS	SUBCLASS	CLASS	SUBCLASS (ONE SUBCLASS PER BLOCK)					
INTERNATIONAL CLASSIFICATION								

☐ Continued on Issue Slip Inside File Jacket

<input type="checkbox"/> <b>TERMINAL DISCLAIMER</b>	DRAWINGS			CLAIMS ALLOWED	
	Sheets Drwg.	Figs. Drwg.	Print Fig.	Total Claims	Print Claim for O.G.
<input checked="" type="checkbox"/> The term of this patent subsequent to _____ (date) has been disclaimed.	_____ (Assistant Examiner) (Date)			<b>NOTICE OF ALLOWANCE MAILED</b>	
<input type="checkbox"/> The term of this patent shall not extend beyond the expiration date of U.S. Patent. No. _____	_____ (Primary Examiner) (Date)			<b>ISSUE FEE</b>	
<input type="checkbox"/> The terminal _____ months of this patent have been disclaimed.	_____ (Legal Instruments Examiner) (Date)			<b>ISSUE BATCH NUMBER</b>	

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